

Fig.1

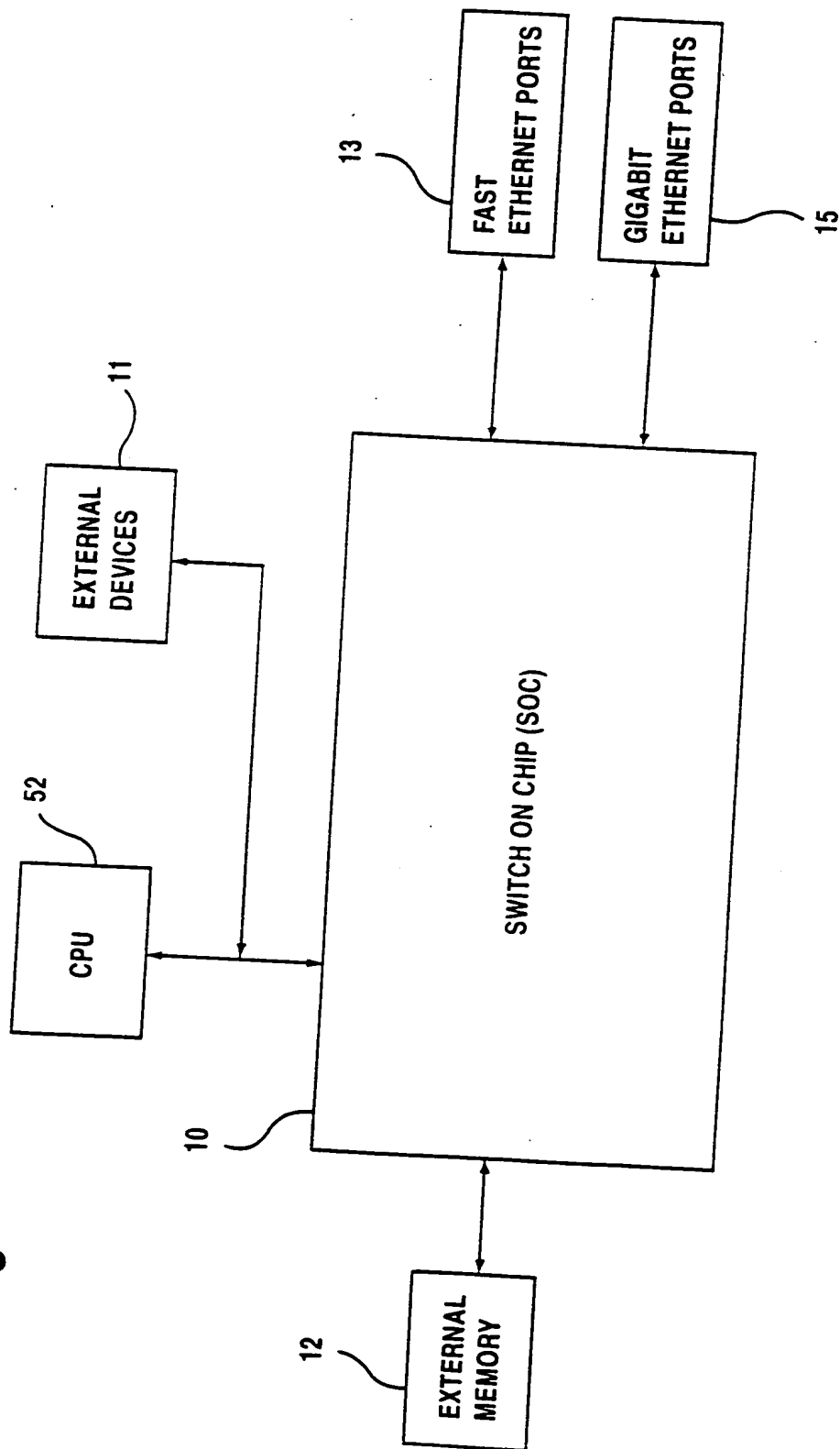
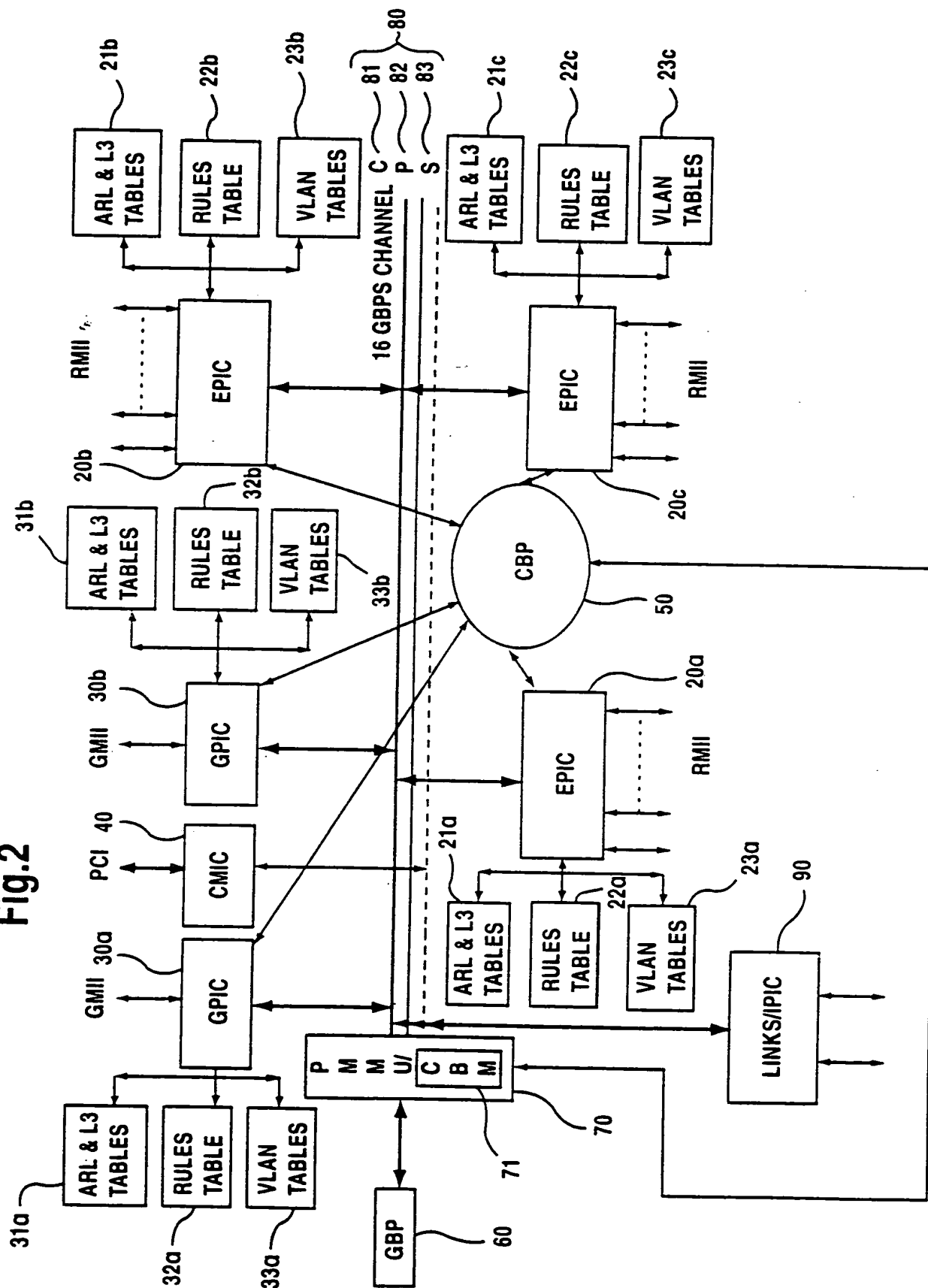


Fig. 2



00450-240450

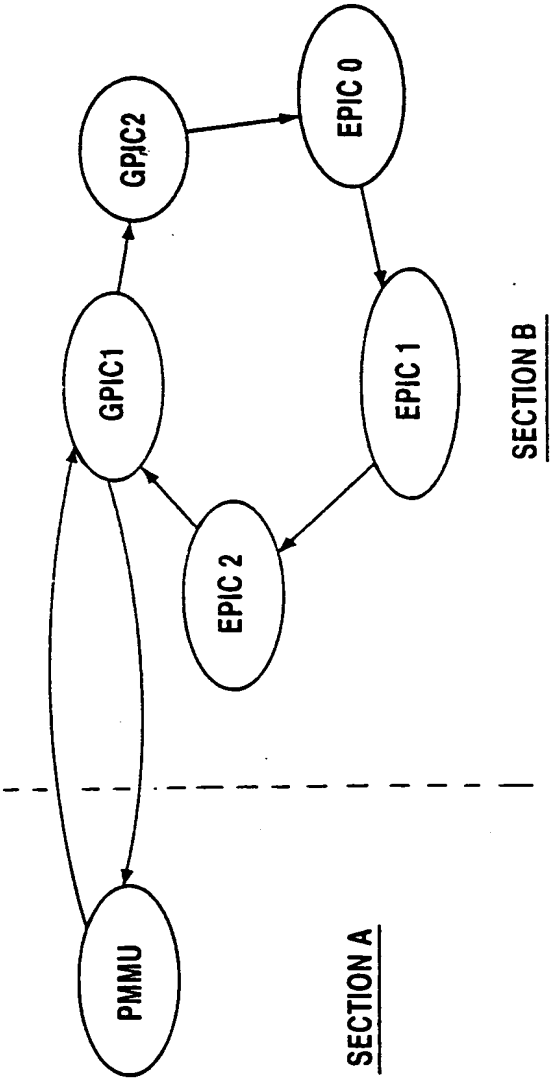


Fig.4a

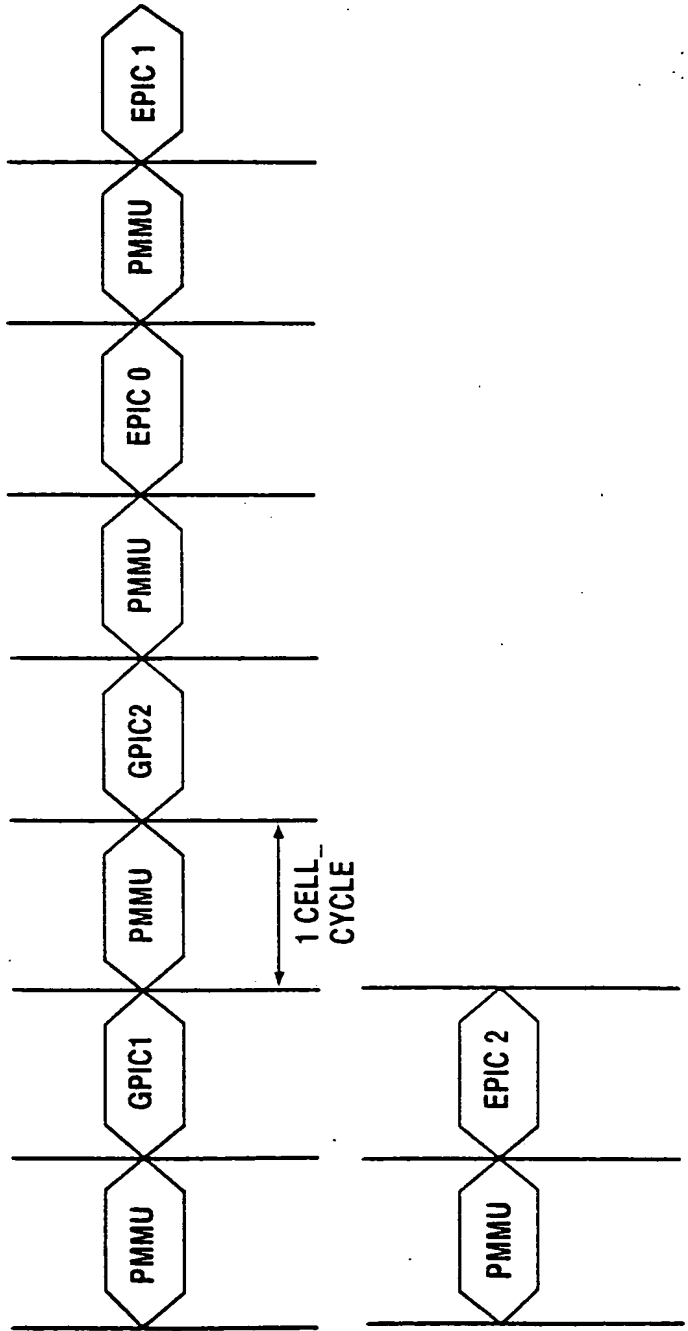


Fig.4b

18.

PROTOCOL CHANNEL MESSAGES

30	28	26	24	22	20	18	16	14	12	10	8	6	4	2	0
OP CODE	I P P X	RESERVED	NXT CELL	SRC DEST PORT			COS		J	S	E	CR P C	O	LEN	

30	28	26	24	22	20	18	16	14	12	10	8	6	4	2	0
RESERVED		BC/MC PORTBITMAP													

30	28	26	24	22	20	18	16	14	12	10	8	6	4	2	0
U	RES	UNTAGGED PORTBITMAP/SRC PORT NUMBER (BIT0..5)													

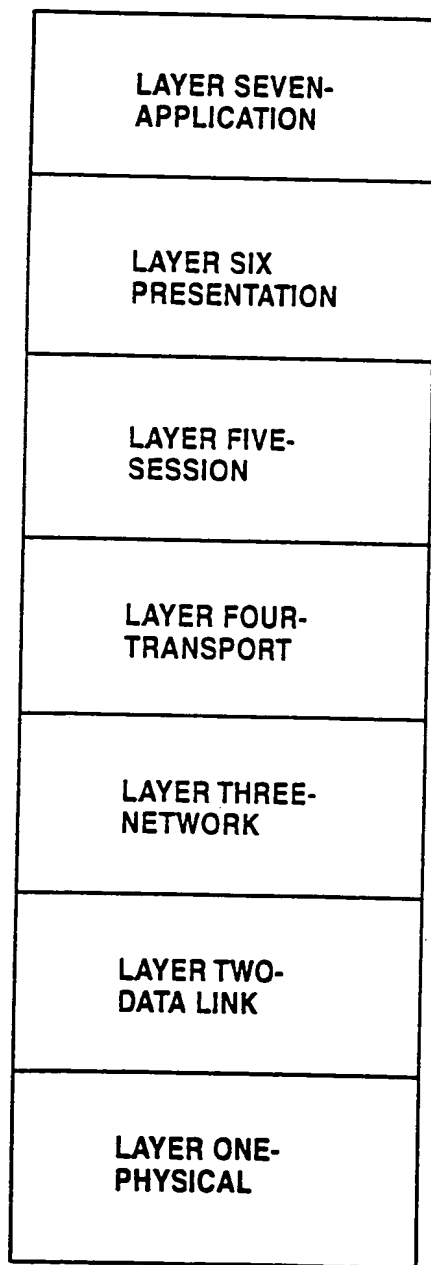
30	28	26	24	22	20	18	16	14	12	10	8	6	4	2	0
CPU OPCODES									TIME STAMP						

Fig.6

SIDE BAND CHANNEL MESSAGES

30	28	26	24	22	20	18	16	14	12	10	8	6	4	2	0		
OPCODE			DEST PORT/ DESTINATION DEV ID			SRC PORT			DATA LEN				E	E CODE		COS	C
ADDRESS																	
DATA																	

Fig.7
PRIOR ART



004250" 21022500

Fig.8

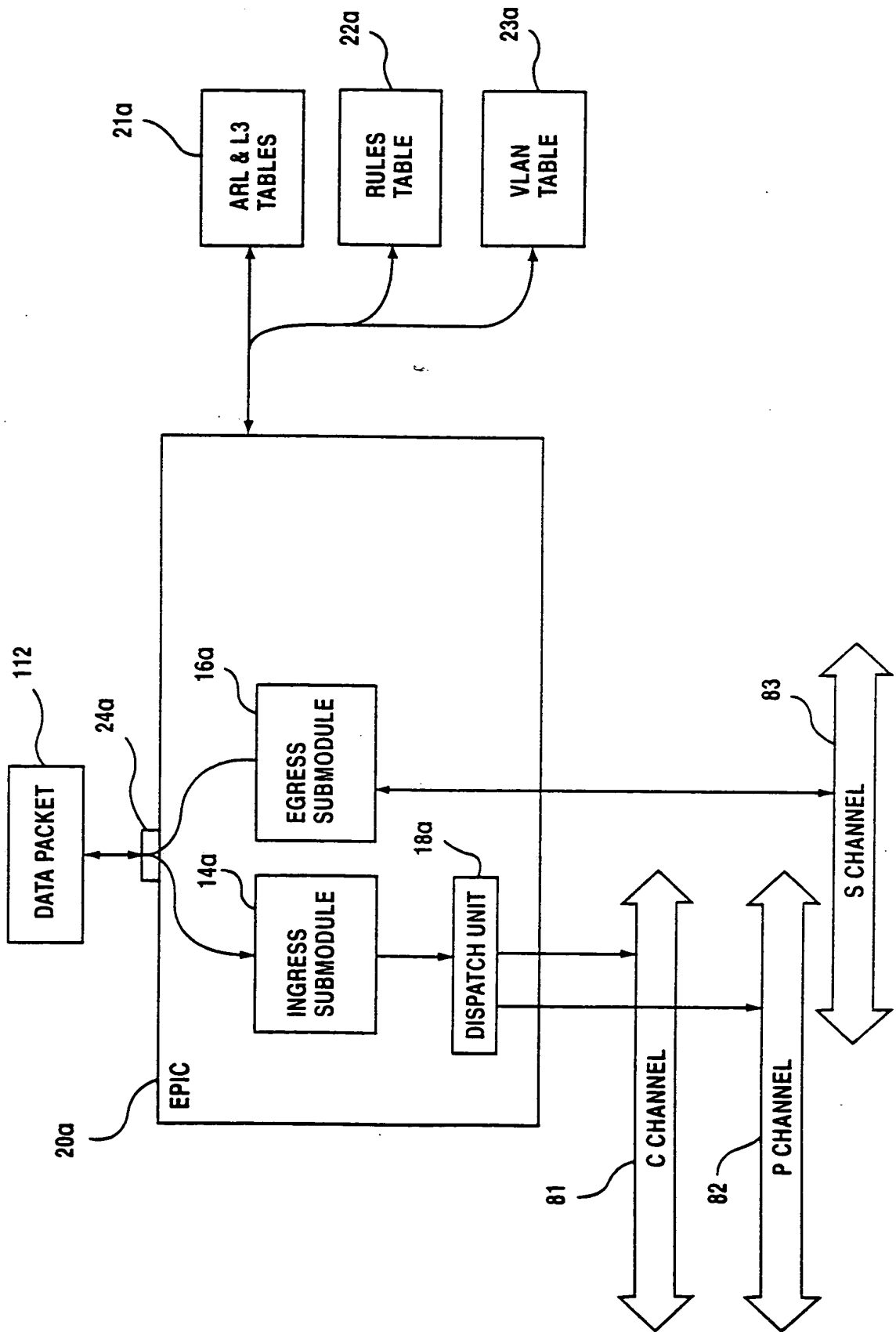
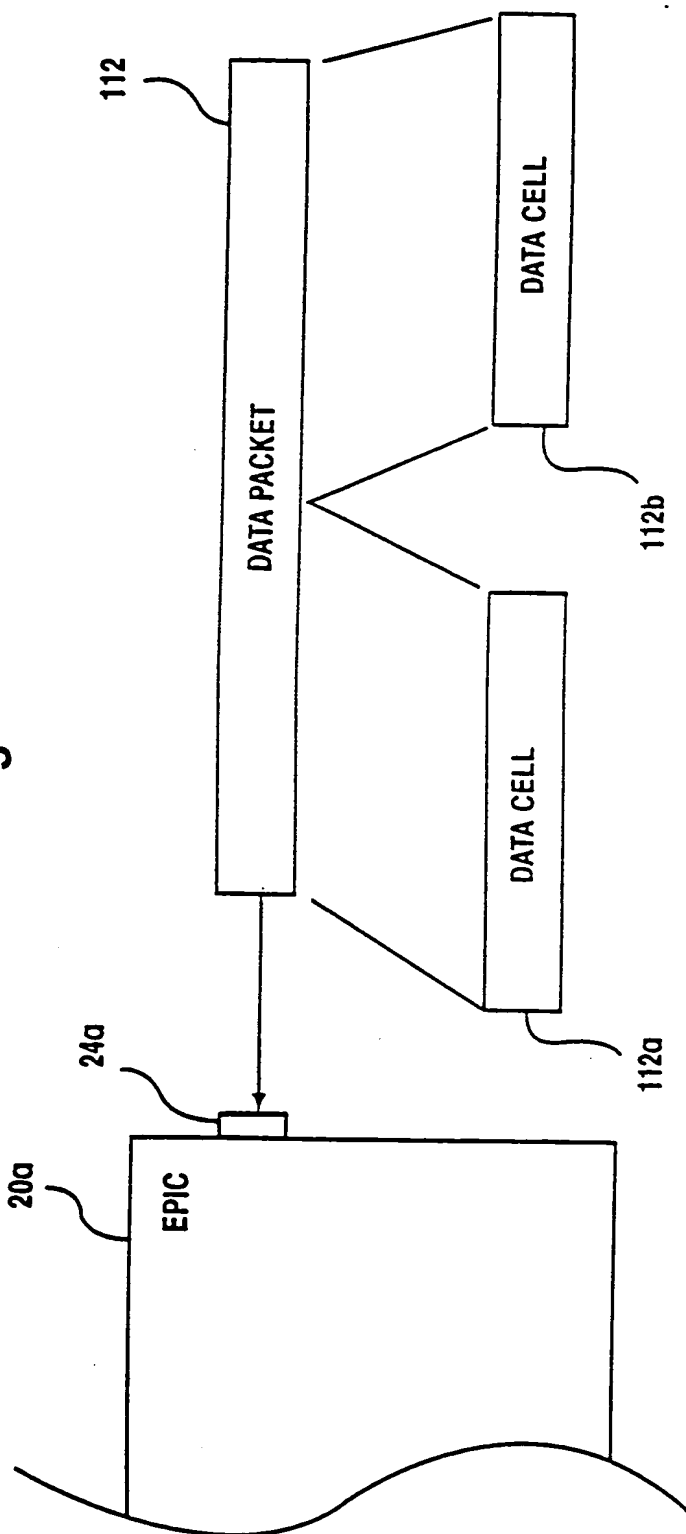


Fig.9



Approved for Release

Fig.10

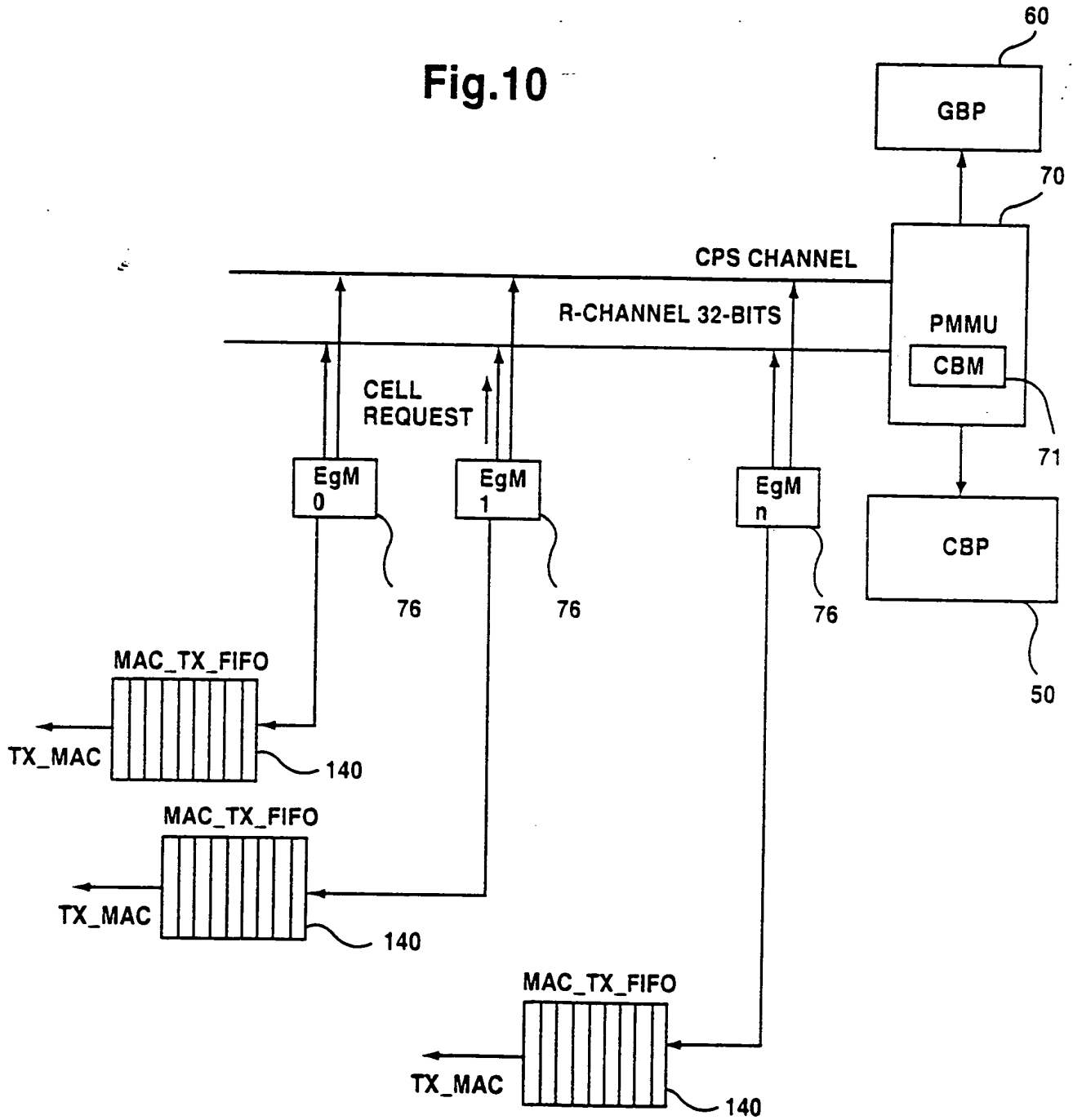


Fig.11

LINE 0 →	FC LC BC/MC CPY_CNT(5b) CELL_LENGTH(7b) CRC(2b) NC_HEADER(16b) SRC_COUNT(6) IPX IP TIME_STAMP(14b) O_BITS(2b) P NEXT_CELL_LEN(2b) CPU_OPCODE(4b) CELL_DATA(0-9B)
LINE 1 →	CELL_DATA(10-27) BYTES
LINE 2 →	CELL_DATA(28-45) BYTES
LINE 3 →	CELL_DATA(46-63) BYTES

Fig.12

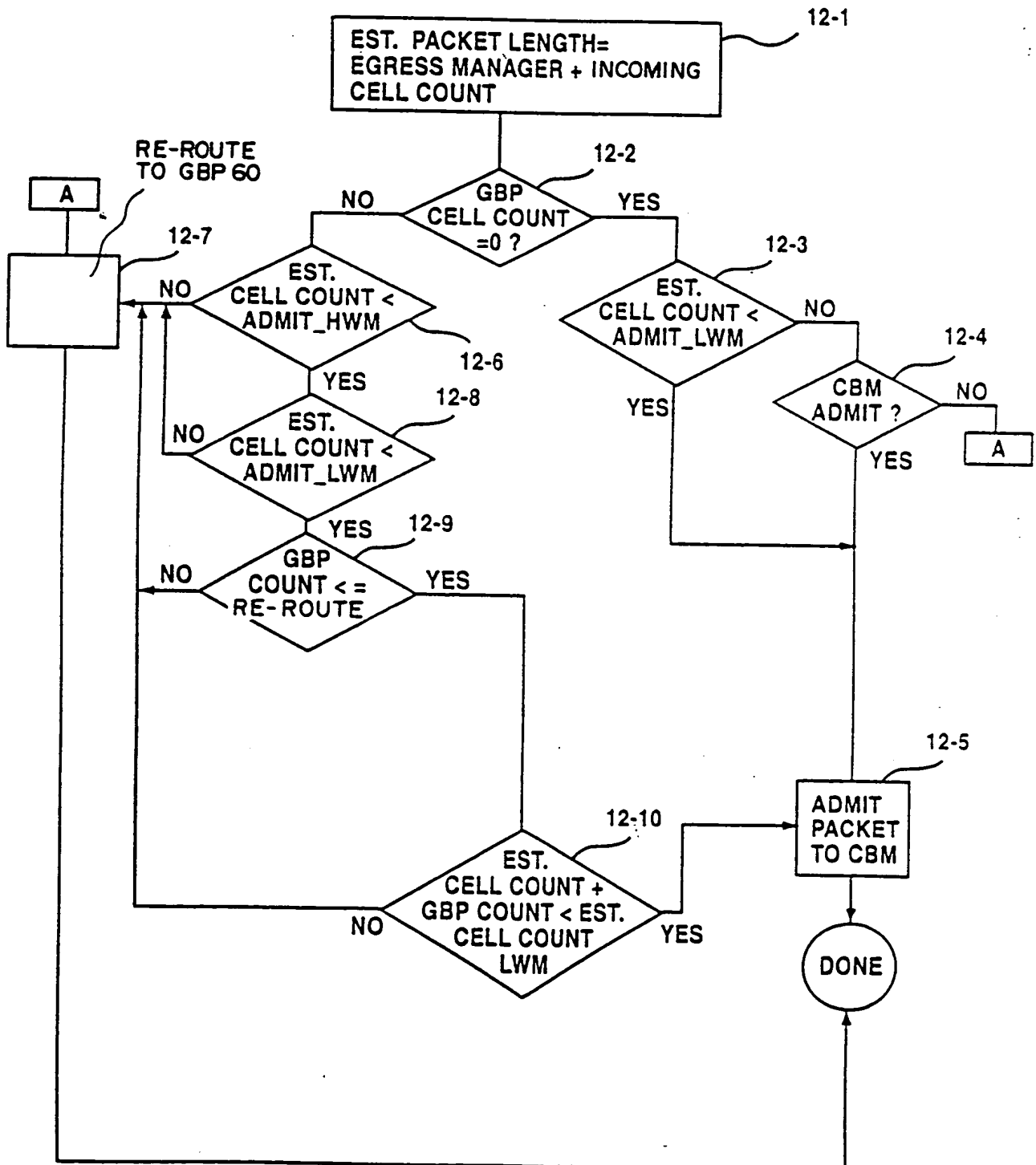


Fig.13

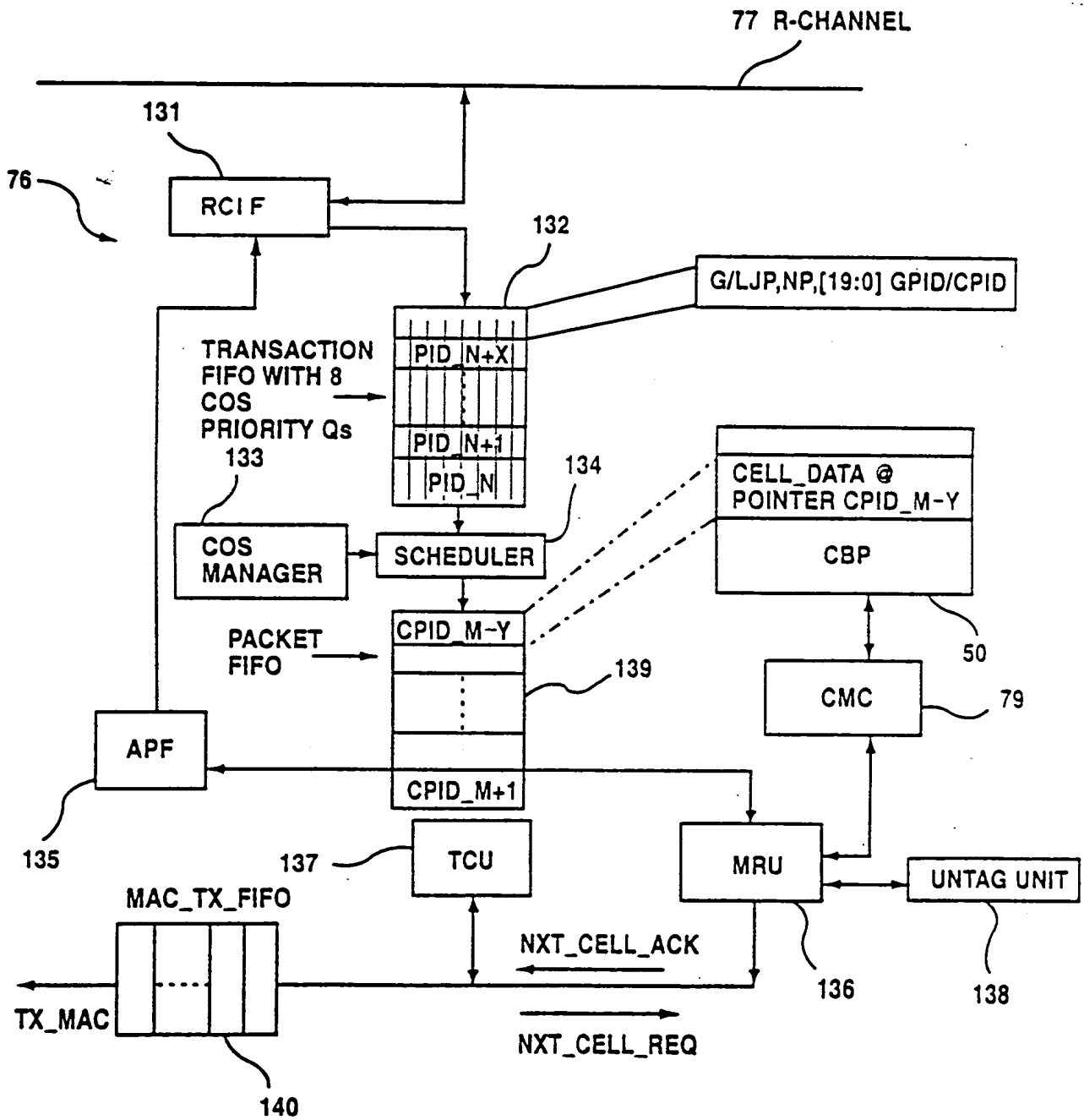


Fig.14

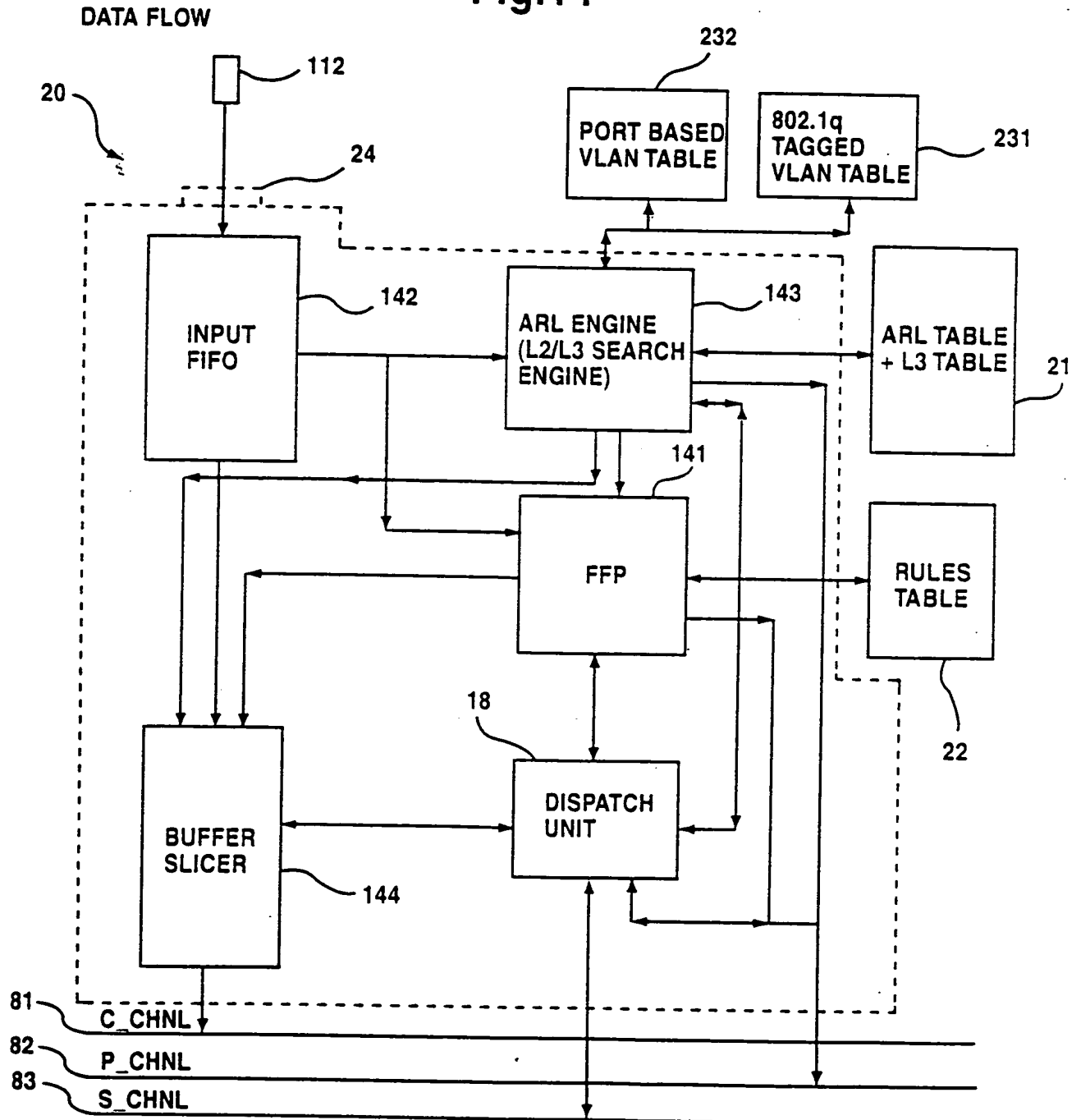
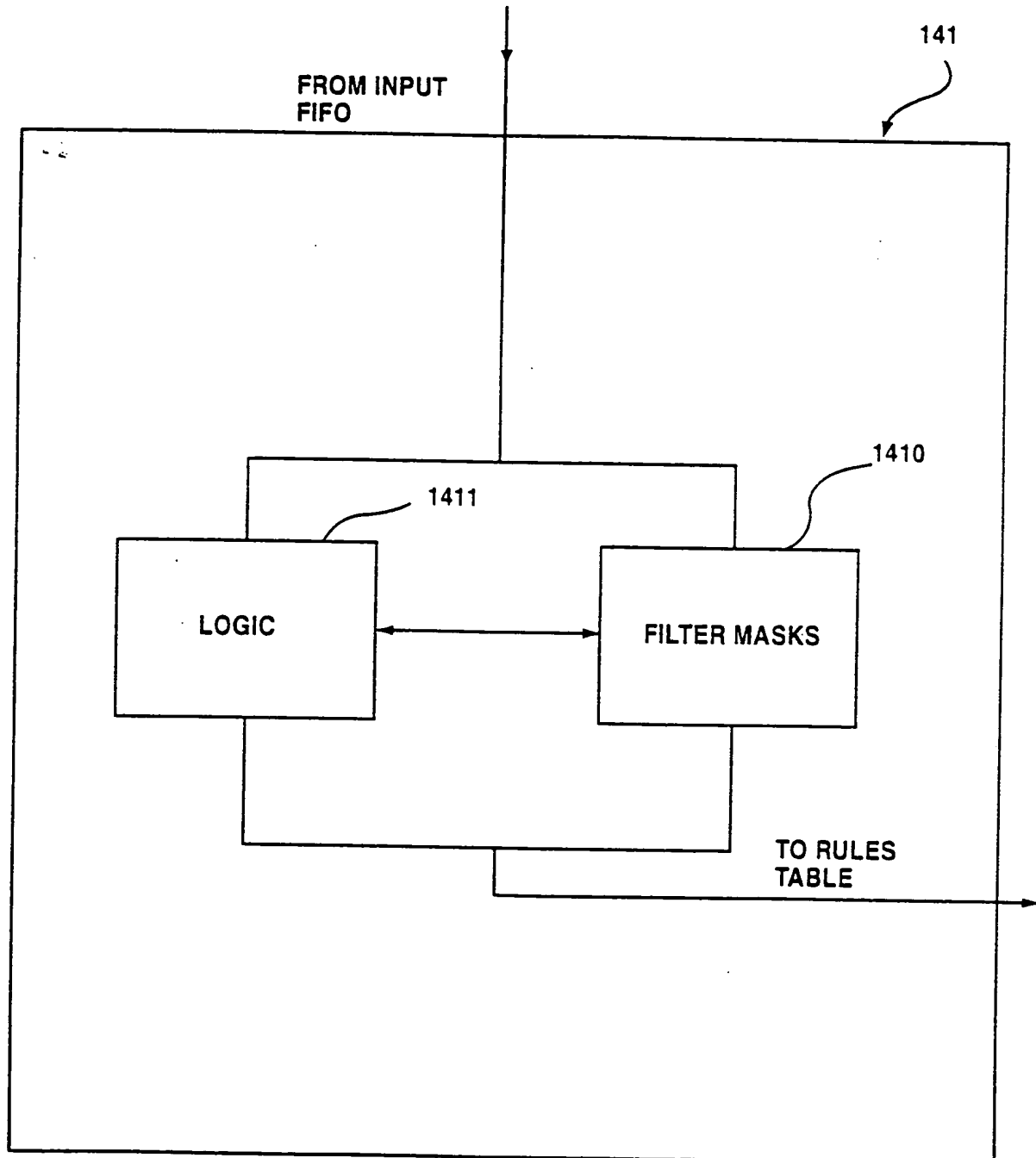


Fig.15



004459 TO 4360

Fig.16

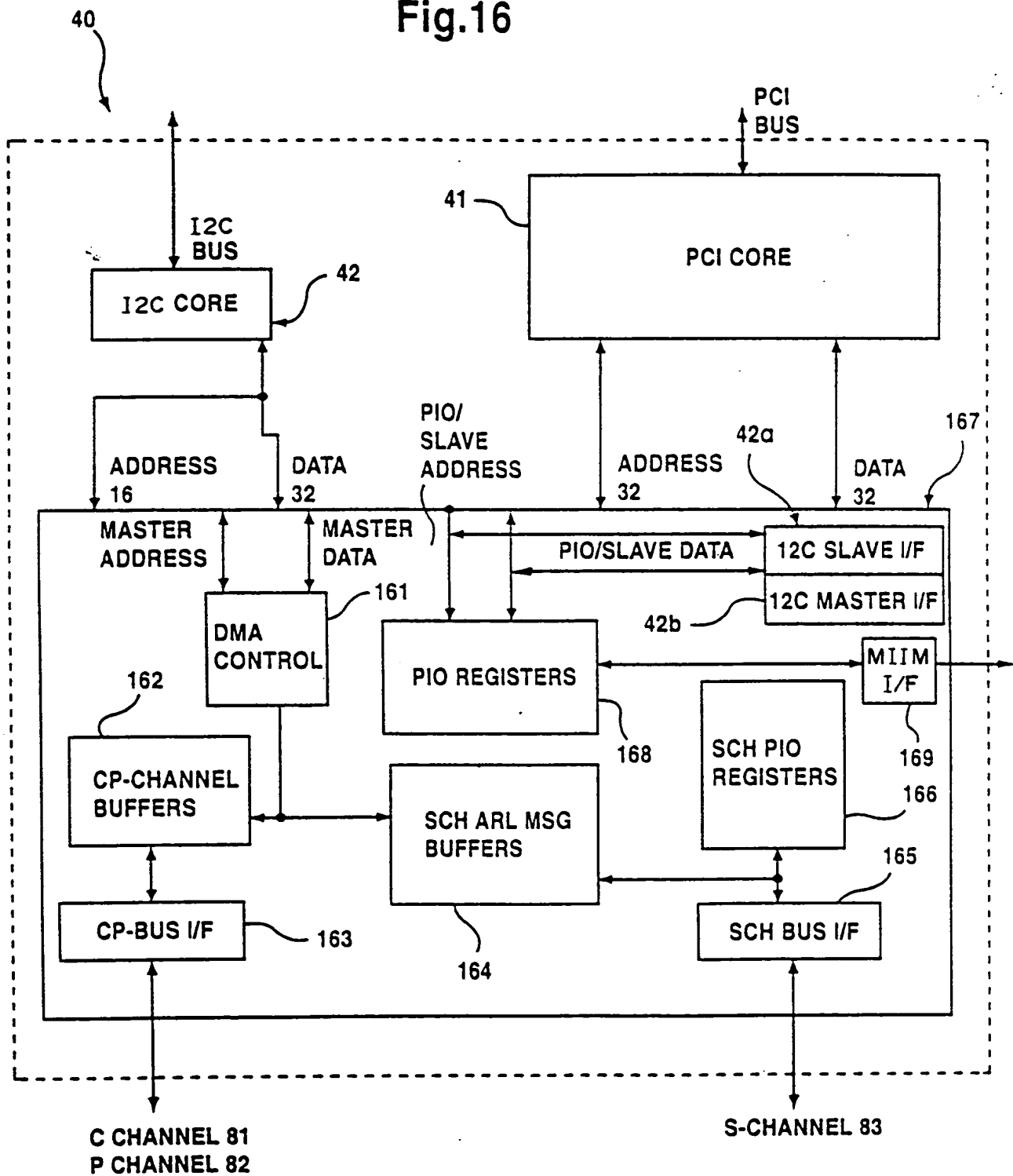


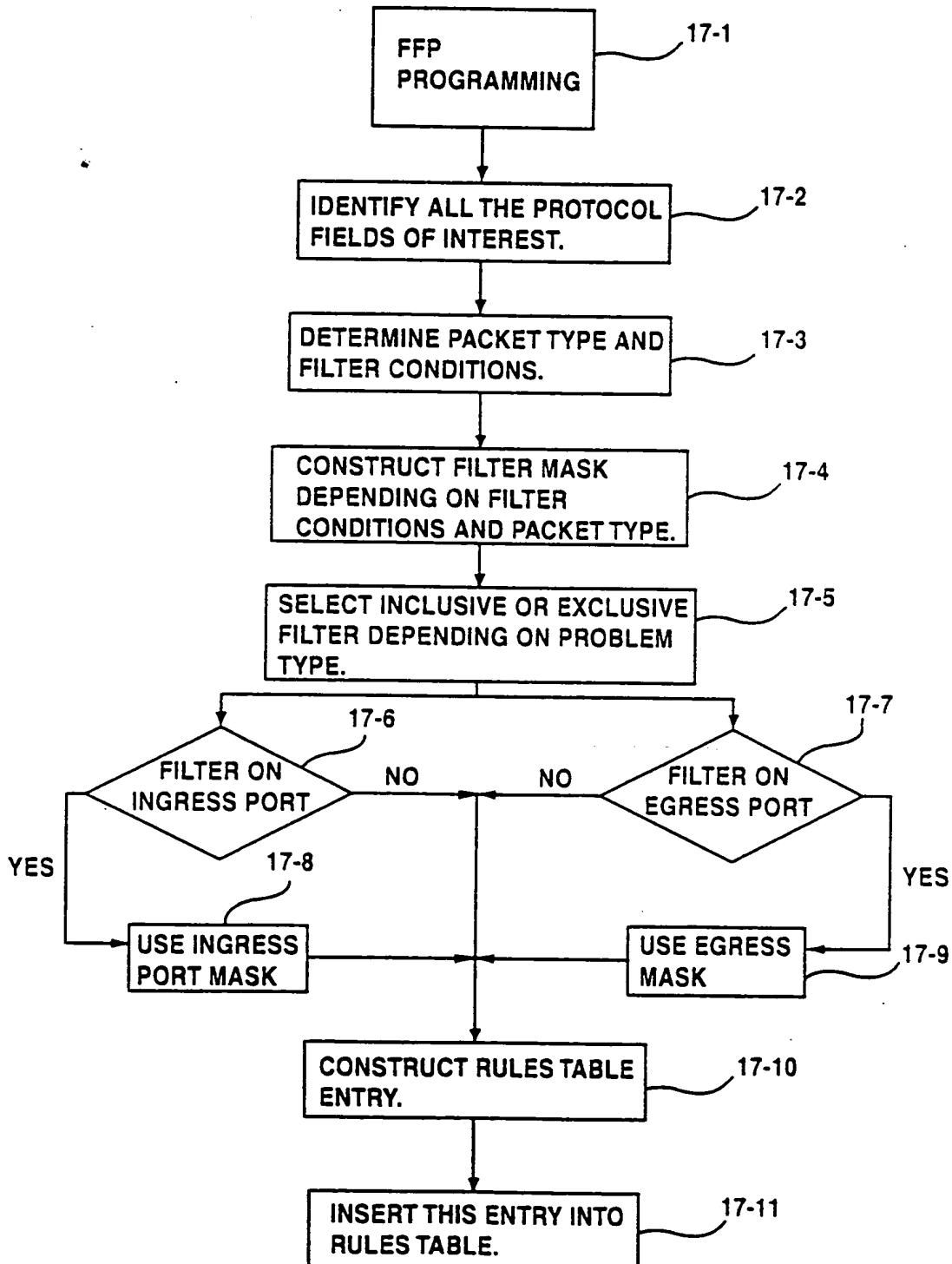
Fig.17**FFP PROGRAMMING FLOW CHART**

Fig.18

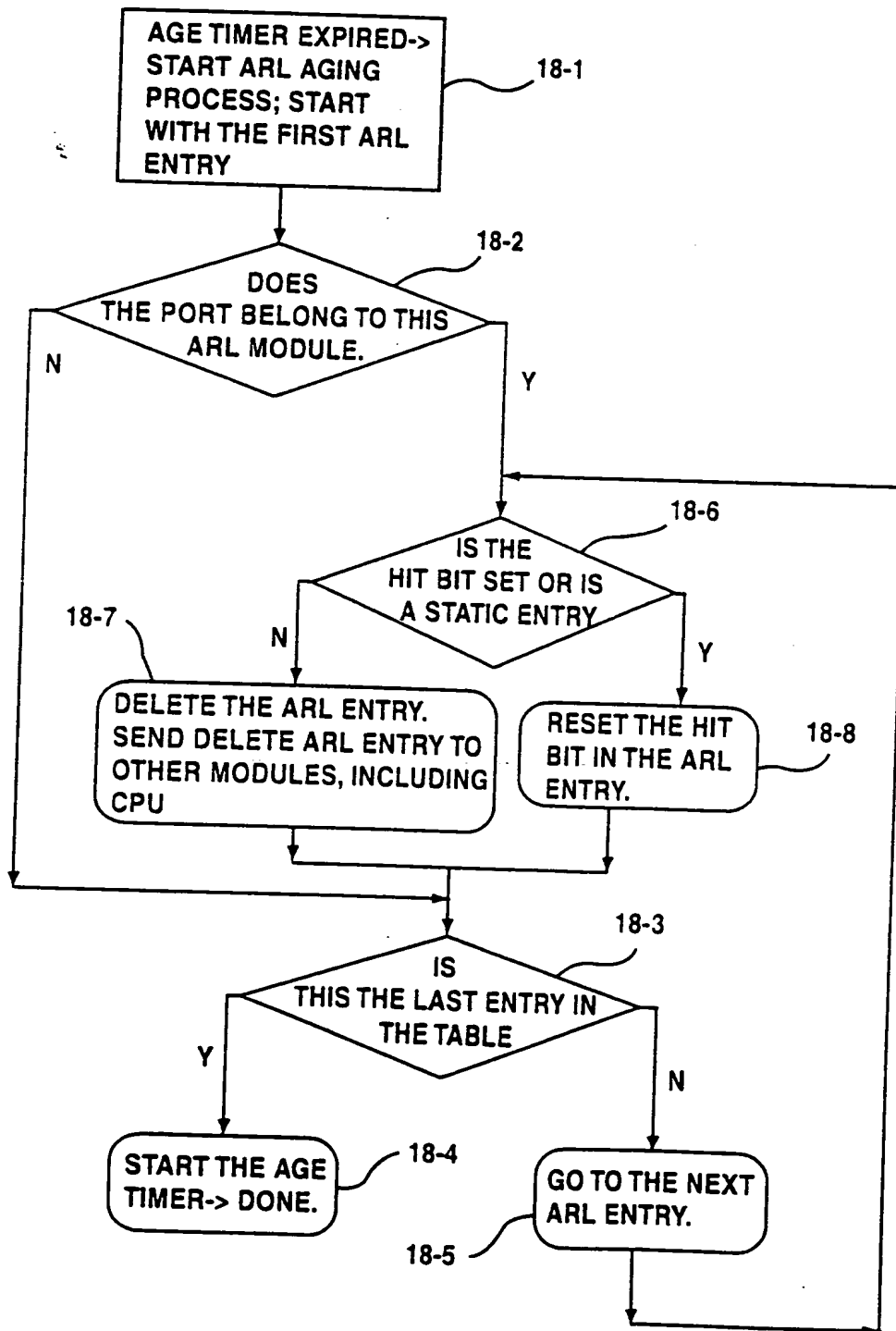
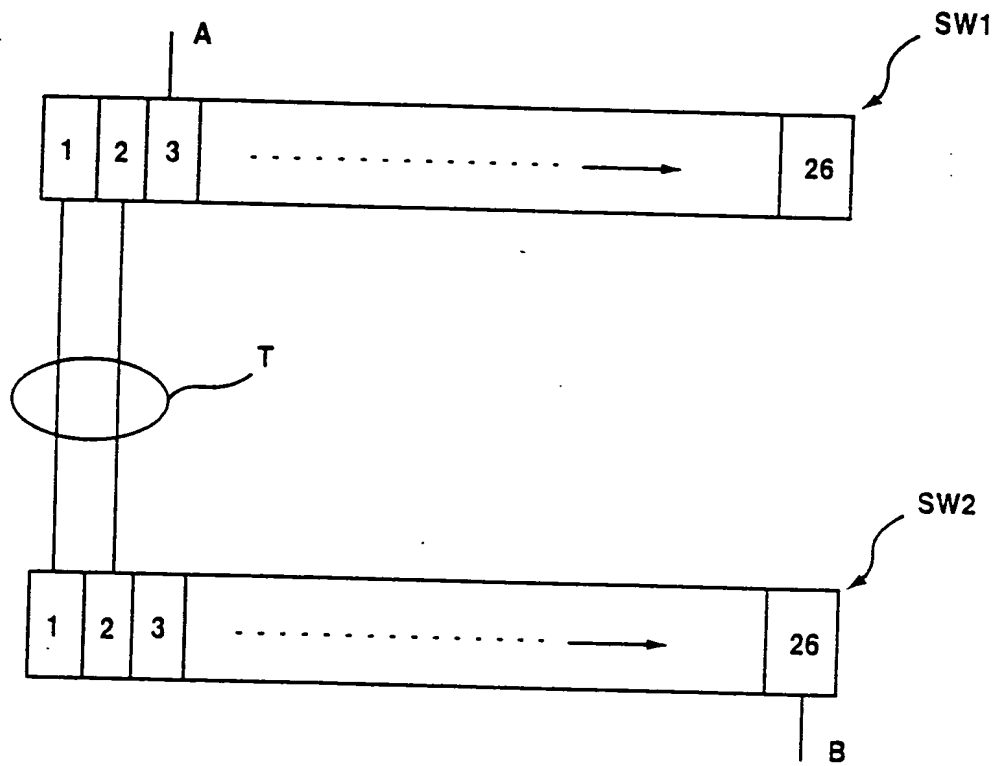
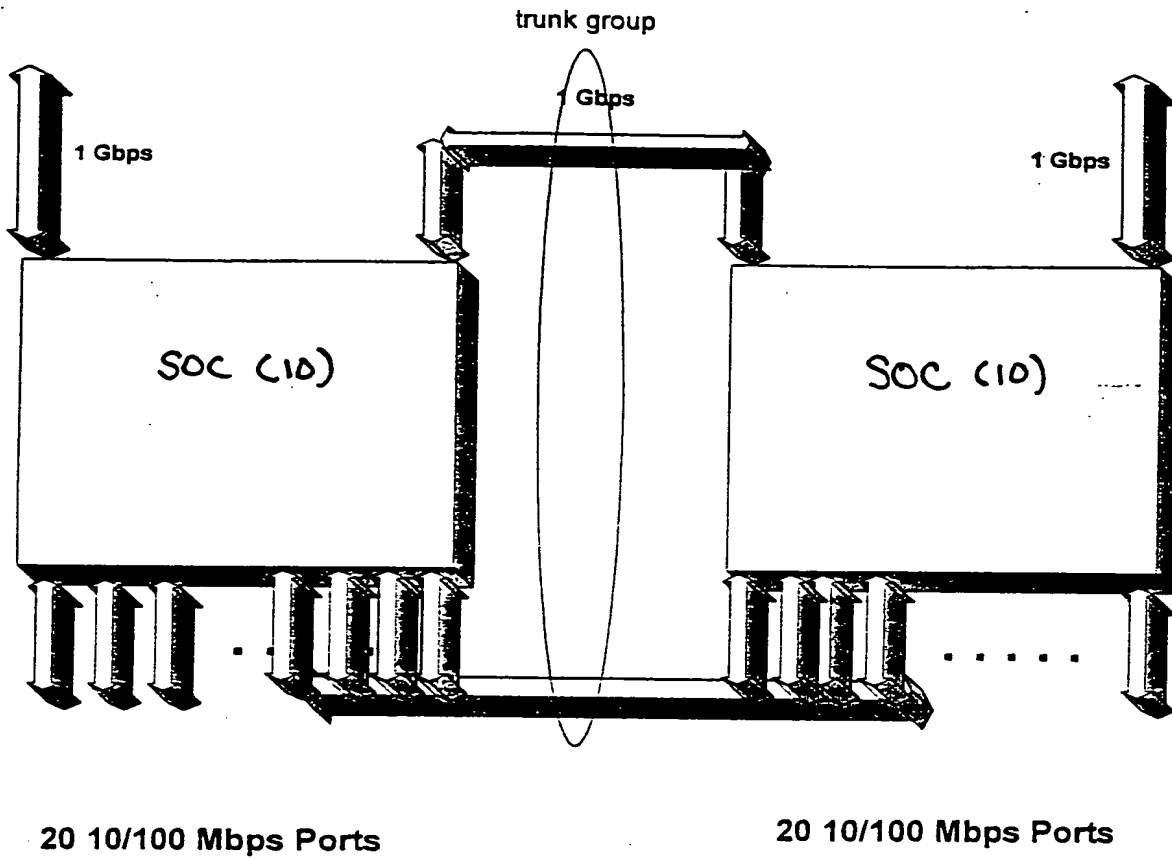


Fig.19





Number of ports = 40, Uplinks = 2 Gigabit ports, Interconnection = 1.4 Gbps using trunk group of 4 FE ports and 1 Gigabit port

FIGURE 20

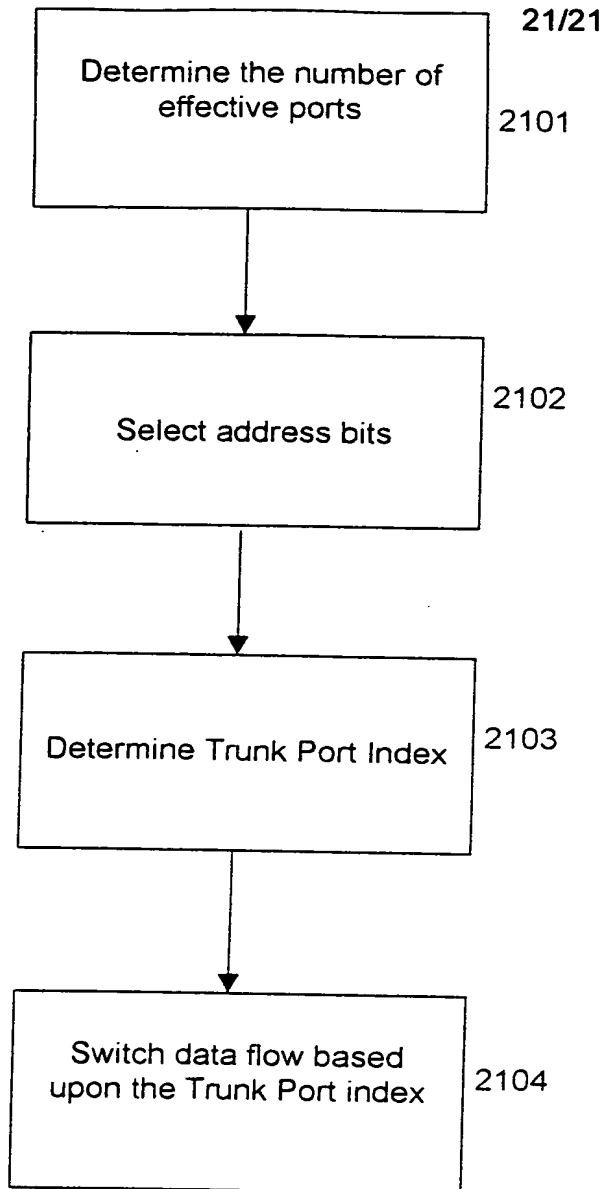


FIGURE 21